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Kristin R. Hayes	(Depositor's name)
<i>Kristin R. Hayes</i>	(Signature)
June 16, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/751,474	12/28/2000	Guenther Lehmann	000P0113 LJS	3514

TITLE OF INVENTION: AREA EFFICIENT STACKING OF ANTIFUSES IN SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/16/2006
EXAMINER	ART UNIT		CLASS-SUBCLASS		
LOKE, STEVEN HO YIN	2811		257-001000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).  
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(A) NAME OF ASSIGNEE

Infineon Technologies AG

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

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Date June 16, 2006

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To: Commissioner for Patents	Total Pages Sent:	3
Mail Stop Issue Fee	(including cover sheet)	
Facsimile Number: (571) 273-2885	Transmission Date:	June 16, 2006

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Lehmann, et al.	Docket No.:	00P0113 US
Serial No.:	09/751,474	Art Unit:	2811
Date Filed:	December 28, 2000	Examiner:	Steven Loke
Title:	Area Efficient Stacking of Antifuses in Semiconductor Device		

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I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (571) 273-2885 on the date shown above:

- Certification of Facsimile Transmission (1 page)
- Part B - Fee(s) Transmittal (2 duplicate pages)

Respectfully submitted,

Kristin R. Hayes  
Legal Assistant

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